

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:)
 Shunpei YAMAZAKI et al.) Art Unit: 2815
 Serial No. 08/520,079) Examiner: P. Brock, II
 Filed: August 28, 1995)
 For: SEMICONDUCTOR CIRCUIT FOR)
 ELECTRO-OPTICAL DEVICE AND METHOD)
 OF MANUFACTURING THE SAME)

48/ID
 4/4/02
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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on February 25, 2002.

Barbara R. Hartz

INFORMATION DISCLOSURE STATEMENT
AND NOTICE OF RELATED APPLICATIONS

Commissioner for Patents and Trademarks
 Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application.

The references listed on the attached Form PTO-1449 have been cited in related U.S. Patent Application Serial No. 09/726,336.

Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a). The requisite fee of \$180.00 is also submitted herewith in accordance with 37 C.F.R. 1.97(c).

Further, pursuant to Applicant's duty of candor under 37 C.F.R. §1.56, Applicants wish to inform the Examiner of the following application claiming related subject matter and hereby submit a copy of the following application for the Examiner's consideration:

<u>SERIAL NO.</u>	<u>FILING DATE</u>	<u>INVENTORS</u>
09/726,336	December 1, 2000	Yamazaki et al.

03/11/2002 JBALINAN 00000030 08520079

01 FC:126

180.00 DP

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380 (740756-1400).

TYPE JC

Respectfully submitted,



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Form PTO-1449
(Rev. 8-83)U.S. Department of Commerce
Patent and Trademark Office

Atty Docket 0756-1400

Serial No. 08/520,079

INFORMATION DISCLOSURE STATEMENT

Applicants: Shunpei YAMAZAKI et al.

Filing Date: August 28, 1995

Group Art Unit: 2815

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date (if appropriate)
<i>R53</i>	5,313,075	05/17/1994	Zhang et al.			
	5,315,132	05/24/1994	Yamazaki			
	5,572,046	11/05/1996	Takemura			
	5,648,662	07/15/1997	Zhang et al.			
	5,744,822	04/28/1998	Takayama et al.			
	5,811,328	09/22/1998	Zhang et al.			
	5,818,068	10/06/1998	Sasaki et al.			
<i>R53</i>	5,883,016	03/16/1999	Chan et al.			
	5,985,741	11/16/1999	Yamazaki et al.			

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
<i>R53</i>	05-335572	12/17/1993	JP			Eng Abst

OTHER DOCUMENT (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initial	
<i>R53</i>	Inventors: Shunpei Yamazaki et al., "Thin Film Type Monolithic Semiconductor Device", Filed: December 1, 2000, Specification & Drawings of U.S. Patent Application Serial No. 09/726,336

Examiner

Date Considered

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

02/25/2002